

**Amendments to the Specification**

Please amend the following paragraph starting on page 1, paragraph [0001], before  
“BACKGROUND.”

**CROSS REFERENCE TO RELATED APPLICATIONS**

This application is a 35 U.S.C. §371 National Stage filing of International Patent Application No. PCT/US05/06142, titled CONTROLLING REMOVAL RATE UNIFORMITY OF AN ELECTROPOLISHING PROCESS IN INTEGRATED CIRCUIT FABRICATION, filed February 23, 2005; the present application which claims the benefit of U.S. Provisional Application No. 60/546,848, filed February 23, 2004, which is incorporated herein by reference in its entirety, and U.S. Provisional Application No. 60/551,632, filed March 7, 2004, which is incorporated herein by reference in its entirety.